

DA DB DC

LOAD DESCRIPTION	LOAD (VA)		
	# A	# B	# C
PANELBOARD UPS	5028		
VIA BYPASS		4594	
SWITCH			4992
SPACE	-		
"		-	
"			-
SUBTOTAL 7	5028	4594	4992

PANEL RDPM 20BY / 120 V,  
3 0, 4 W, 100A FRAME, 100A I.R.P. MCB  
 SHORT CIRCUIT RATING = 10 KA  
 NON-LINEAR TYPE WITH 200% NEUTRAL.

DA DB DC

TOTAL	
35	
82	

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LOAD DESCRIPTION	LOAD (AMPS)	
	"A"	"B"
BATTERY STRING NUMBER 3	22	
		22
BATTERY STRING NUMBER 4	22	
		22
SPARE	-	-
	-	-
SUBTOTAL	44	44

PANELBOARD "DC"  
275A VLO, 250 VDC  
SHORT CIRCUIT RATING = 5KA  
SQUARE D 3-WIRE" WITH FA24080(4B)  
CIRCUIT BREAKERS, OR APPROVED EQUAL

## DA DB DC

Figure 1 illustrates a 48-bit bus system. The bus is represented by a vertical line with bit numbers 1 through 48 marked on the left. Three 16-bit registers, labeled 6A, 6B, and 6C, are connected to the bus. Register 6A is connected to the first 16 bits (bits 1-16). Register 6B is connected to the next 16 bits (bits 17-32). Register 6C is connected to the final 16 bits (bits 33-48). Arrows indicate the data flow from the registers to the bus.

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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